

Fig. 1A

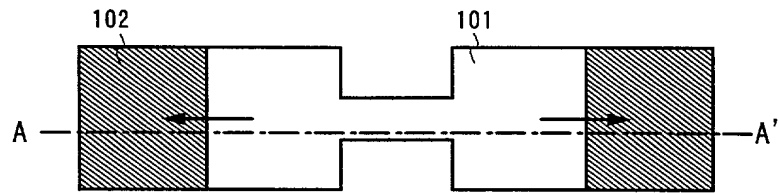


Fig. 1B

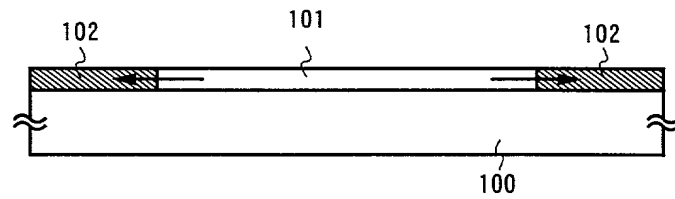


Fig. 1C

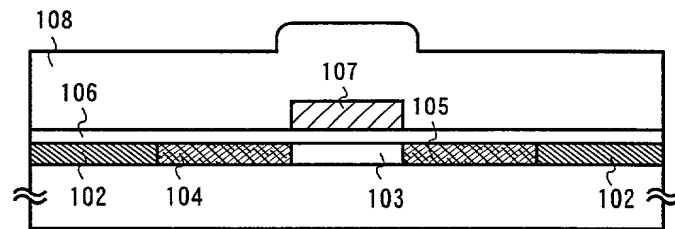


Fig. 1D

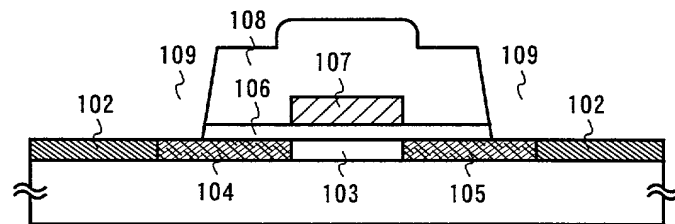


Fig. 1E

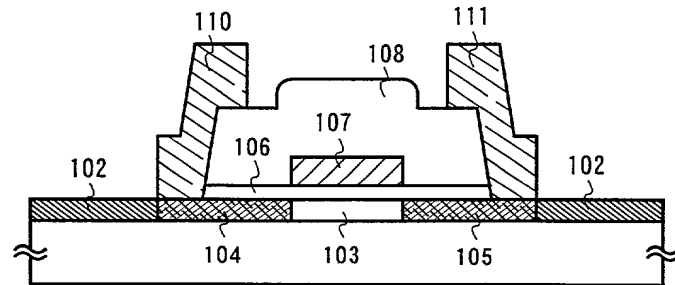
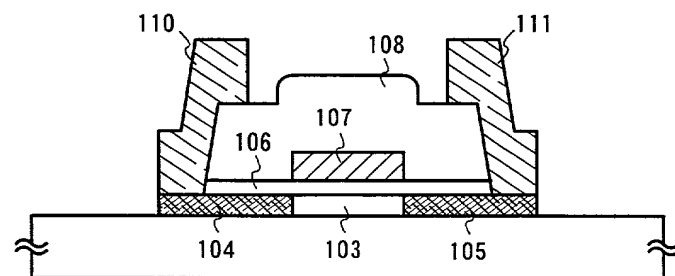


Fig. 1F



P-channel type TFT

Fig. 2A

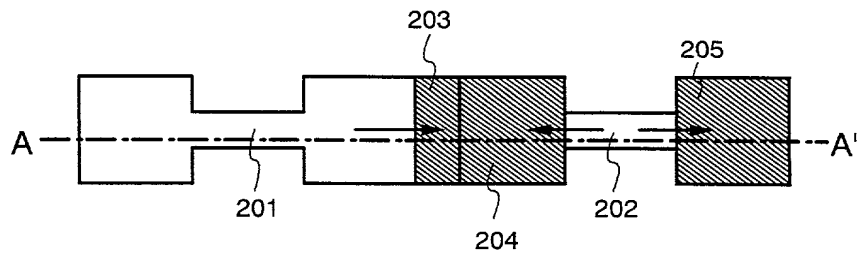


Fig. 2B

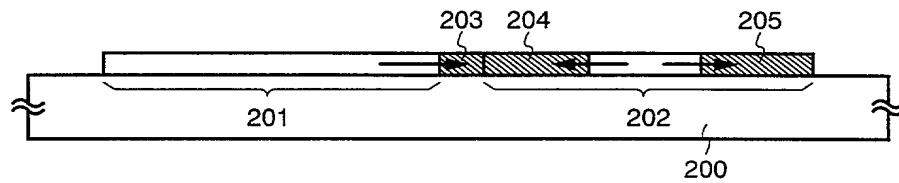


Fig. 2C

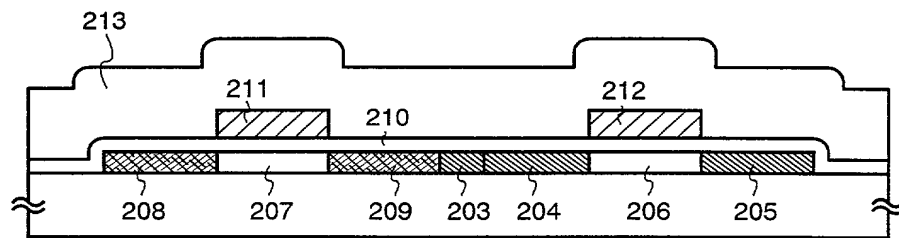


Fig. 2D

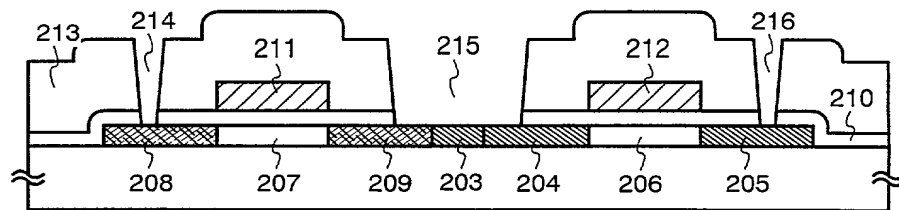


Fig. 2E

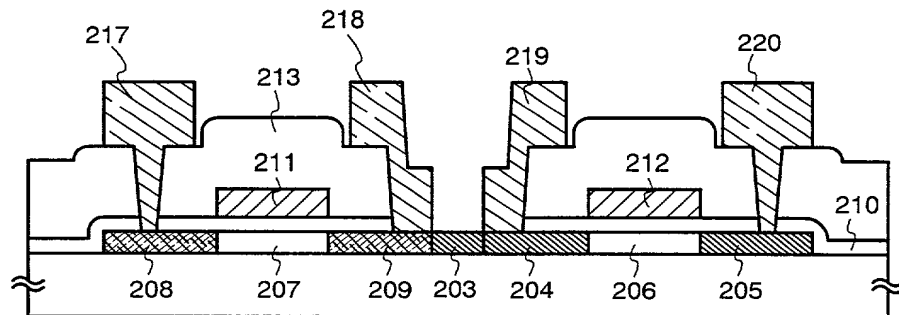
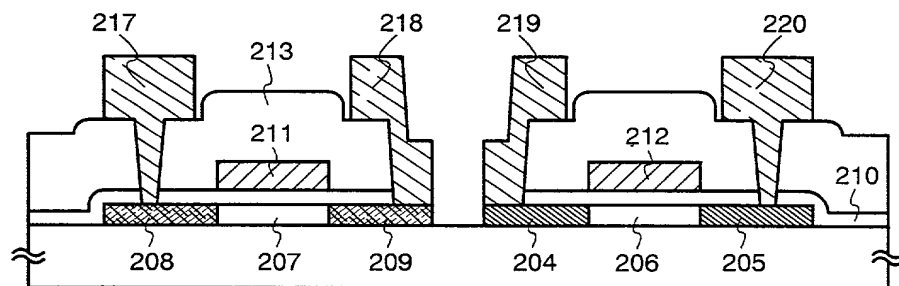


Fig. 2F



P-channel type TFT

N-channel type TFT

Fig. 3A

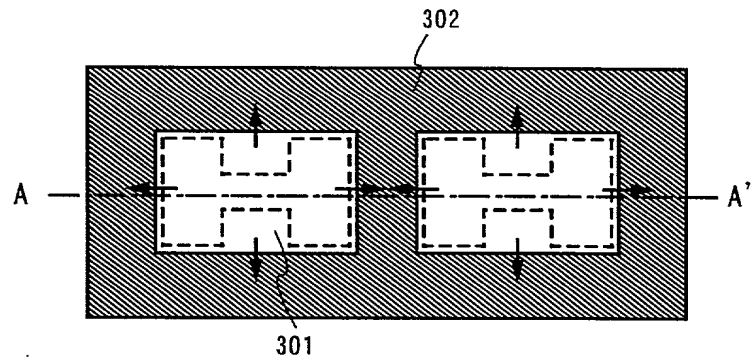


Fig. 3B

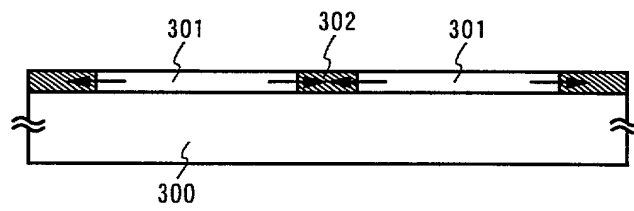


Fig. 4A

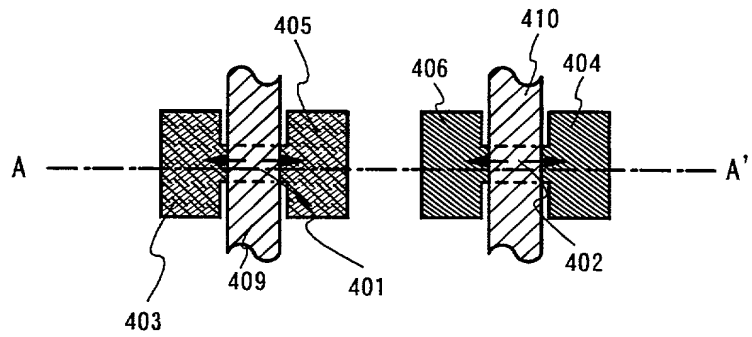
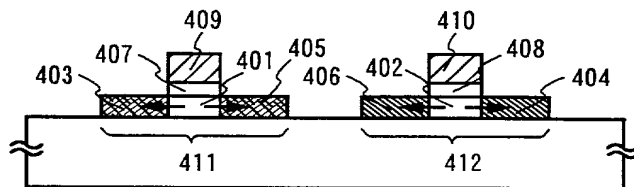


Fig. 4B



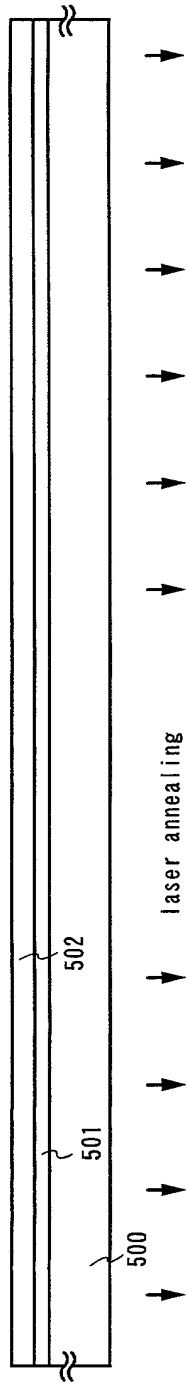


Fig. 5A



Fig. 5B

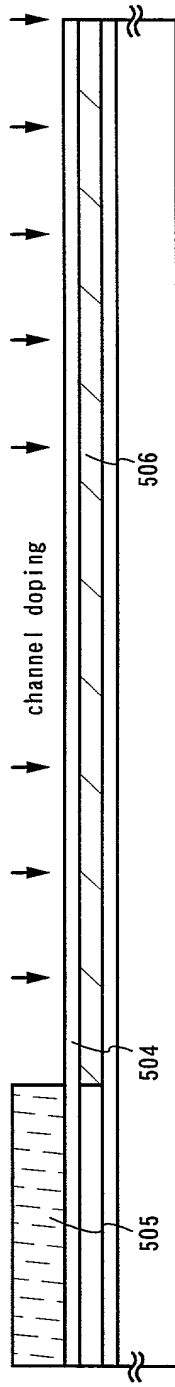


Fig. 5C

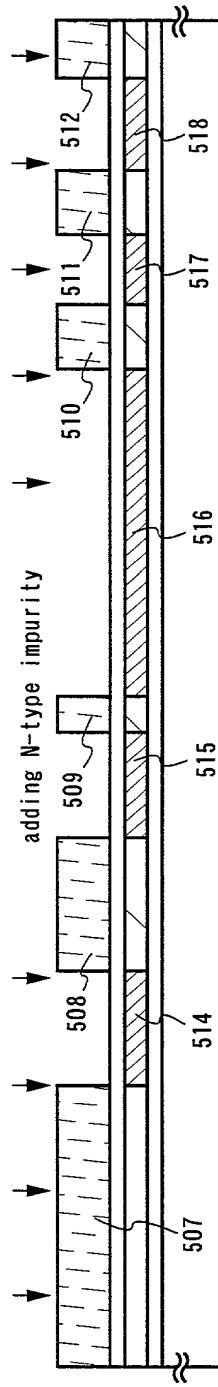


Fig. 5D

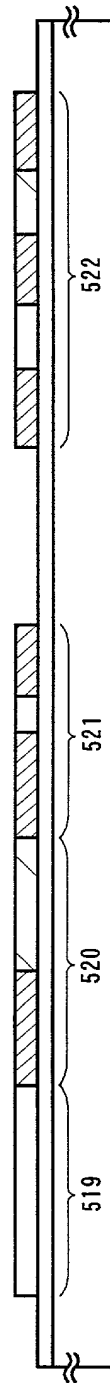


Fig. 5E

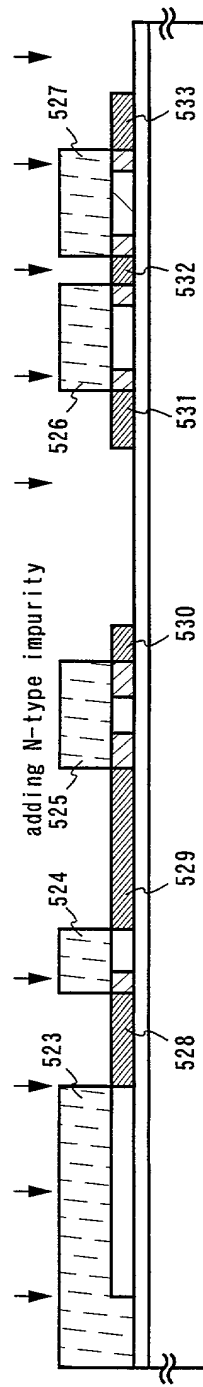


Fig. 5F

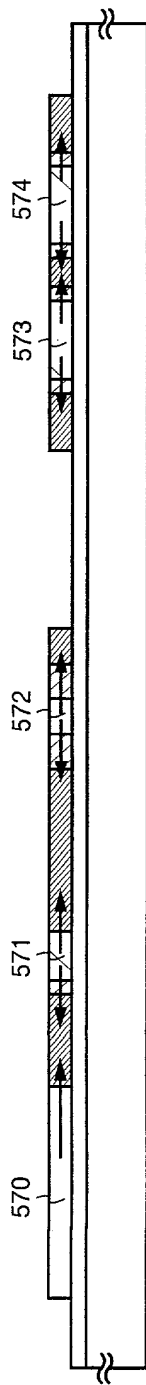


Fig. 6A

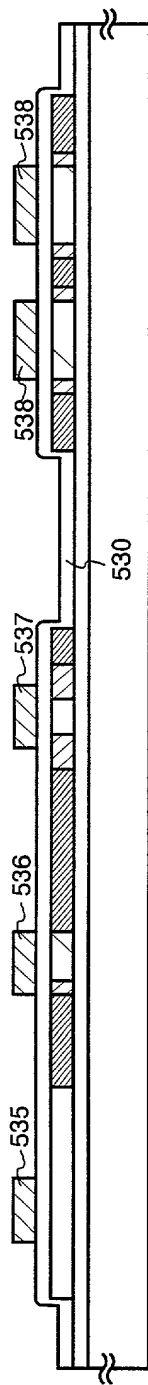


Fig. 6B

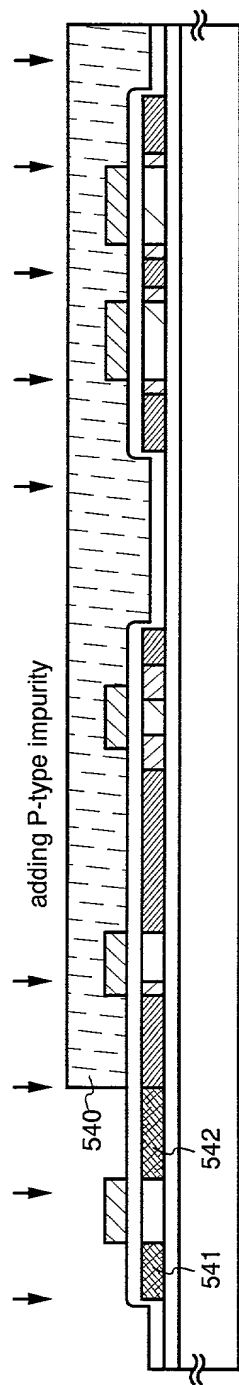


Fig. 6C

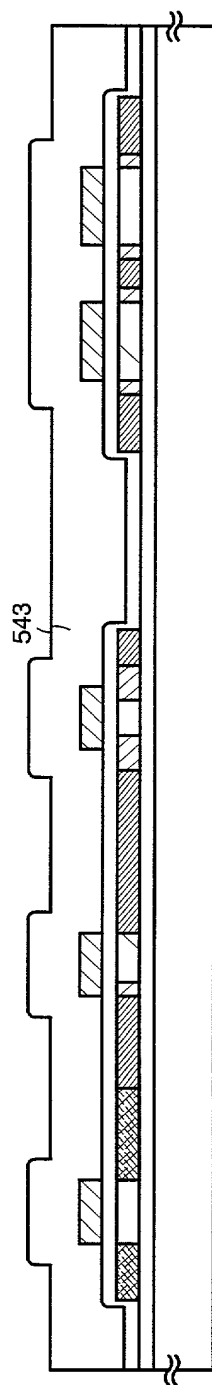


Fig. 6D

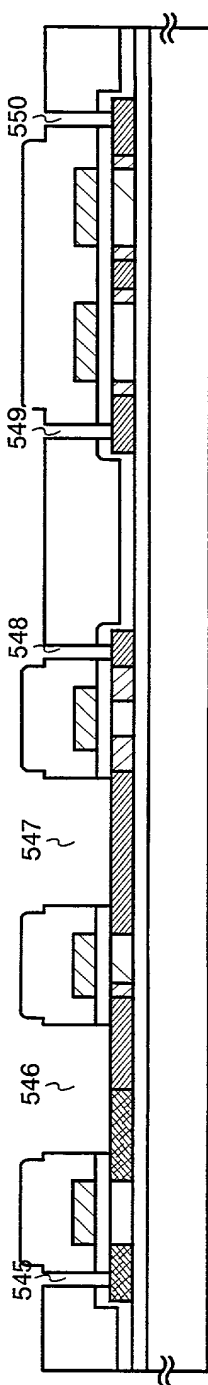


Fig. 6E

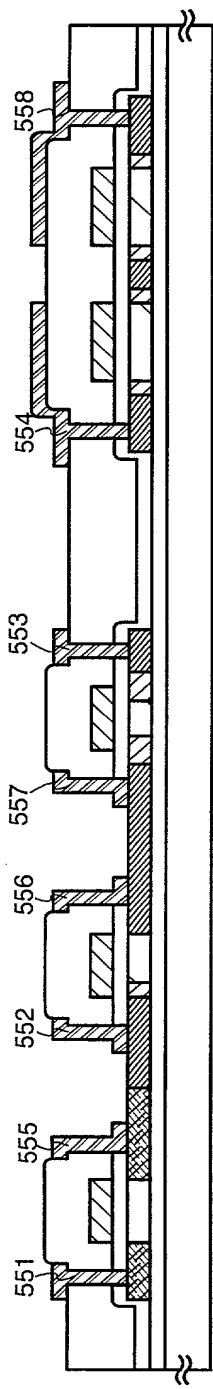


Fig. 7A

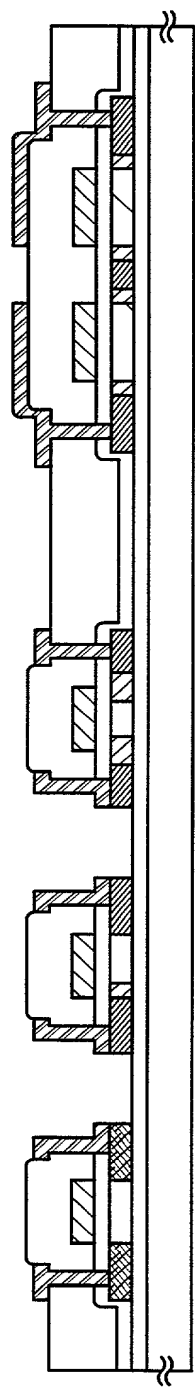


Fig. 7B

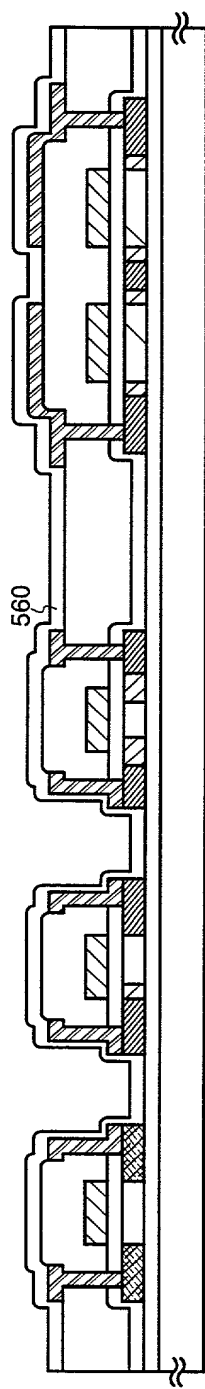


Fig. 7C

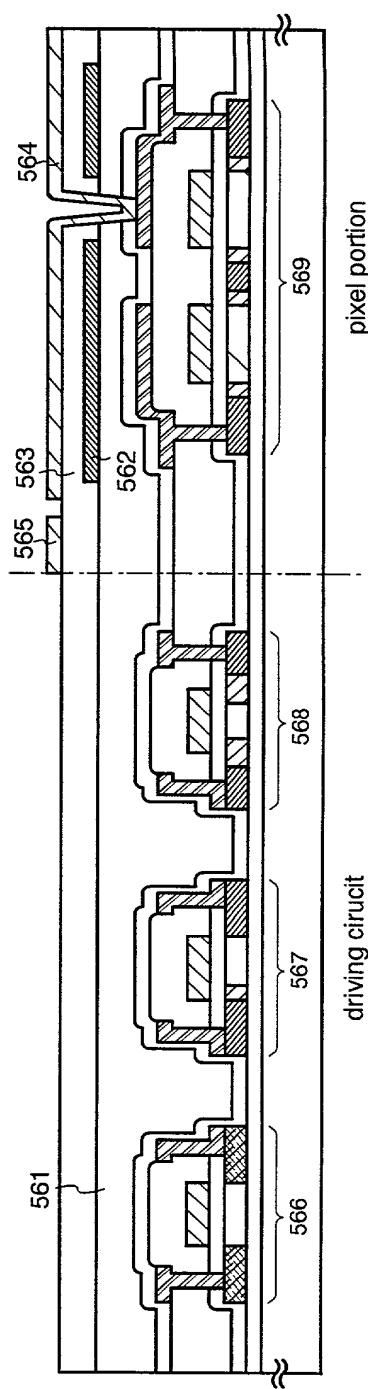


Fig. 7D

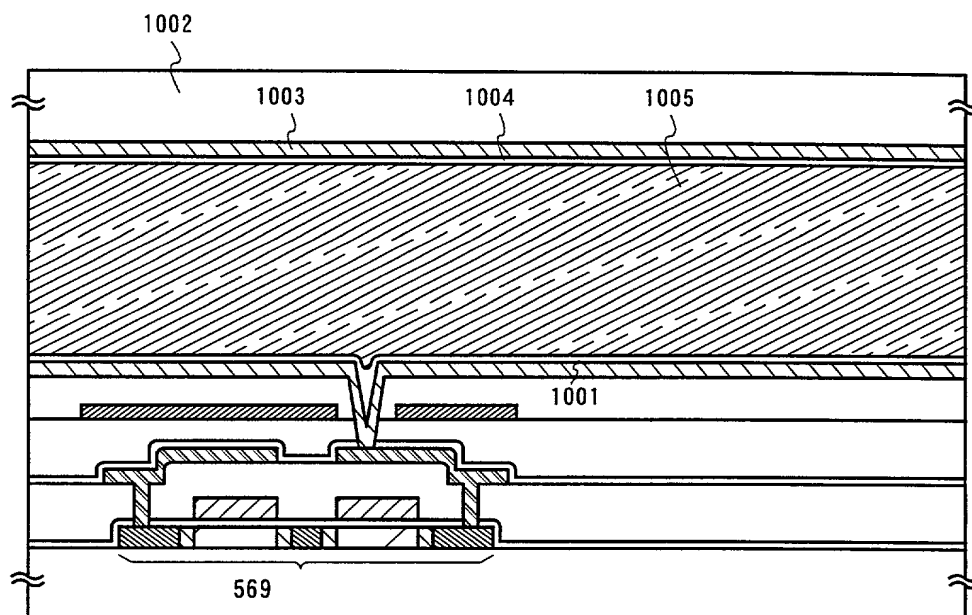
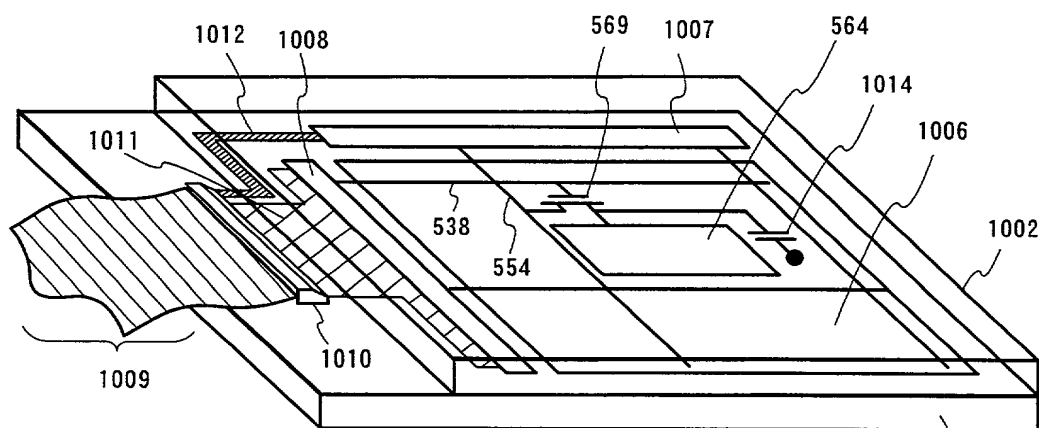


Fig. 8



500:glass substrate 1006:pixel portion 1007:gate
signal line driver circuit 1008: source signal line
driver circuit 1009:FPC 1010:input terminal portion
1011, 1012:connection wiring 569:pixel TFT 538:gate
wiring 554:source wiring 564:pixel electrode
1014:storage capacitor 1002:opposing substrate

Fig. 9

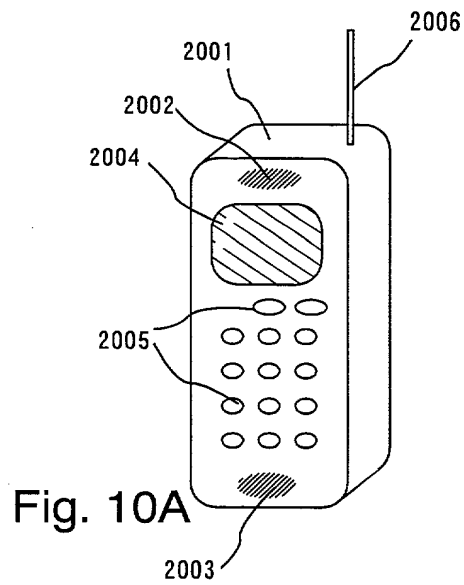


Fig. 10A

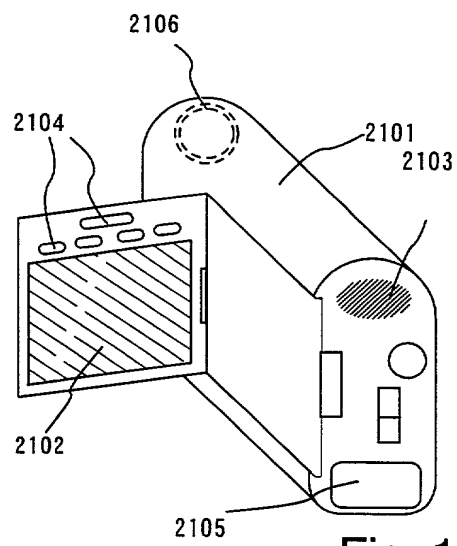


Fig. 10B

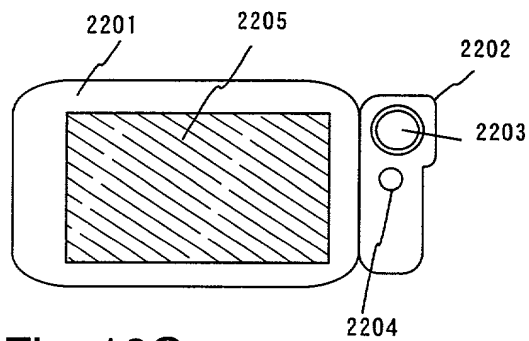


Fig. 10C

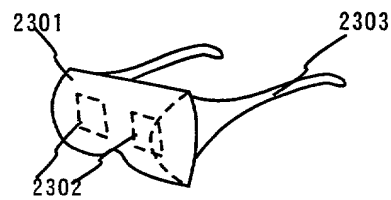


Fig. 10D

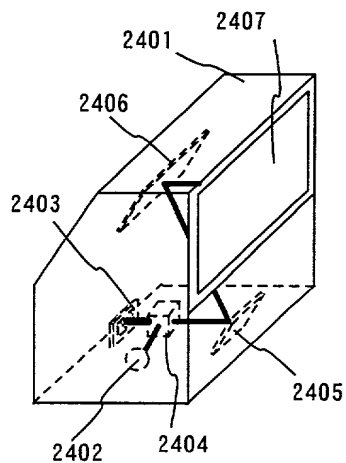


Fig. 10E

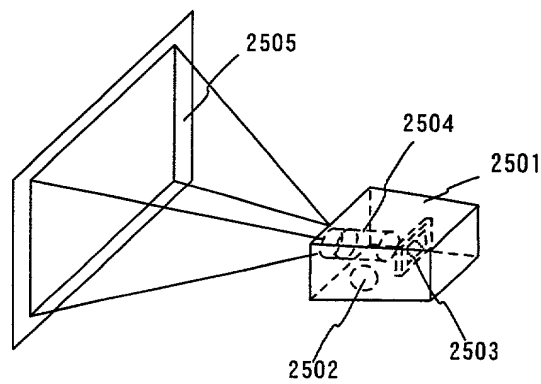


Fig. 10F